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# NOTICE OF ALLOWANCE AND FEE(S) DUE

26200

7590

08/18/2008

FISH & RICHARDSON P.C. P.O BOX 1022 MINNEAPOLIS, MN 55440-1022 EXAMINER
PHU, SANH D

ART UNIT
PAPER NUMBER

2618

DATE MAILED: 08/18/2008

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/762,153	01/20/2004	Xiaopeng Chen	MP0417/13361-070001	4951

TITLE OF INVENTION: METHOD AND APPARATUS FOR REDUCING ECHO AND CROSSTALK IN A COMMUNICATION SYSTEM

APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1440	\$0	\$0	\$1440	11/18/2008

THE APPLICATION IDENTIFIED ABOVE HAS BEEN EXAMINED AND IS ALLOWED FOR ISSUANCE AS A PATENT. PROSECUTION ON THE MERITS IS CLOSED. THIS NOTICE OF ALLOWANCE IS NOT A GRANT OF PATENT RIGHTS. THIS APPLICATION IS SUBJECT TO WITHDRAWAL FROM ISSUE AT THE INITIATIVE OF THE OFFICE OR UPON PETITION BY THE APPLICANT. SEE 37 CFR 1.313 AND MPEP 1308.

THE ISSUE FEE AND PUBLICATION FEE (IF REQUIRED) MUST BE PAID WITHIN THREE MONTHS FROM THE MAILING DATE OF THIS NOTICE OR THIS APPLICATION SHALL BE REGARDED AS ABANDONED. THIS STATUTORY PERIOD CANNOT BE EXTENDED. SEE 35 U.S.C. 151. THE ISSUE FEE DUE INDICATED ABOVE DOES NOT REFLECT A CREDIT FOR ANY PREVIOUSLY PAID ISSUE FEE IN THIS APPLICATION. IF AN ISSUE FEE HAS PREVIOUSLY BEEN PAID IN THIS APPLICATION (AS SHOWN ABOVE), THE RETURN OF PART B OF THIS FORM WILL BE CONSIDERED A REQUEST TO REAPPLY THE PREVIOUSLY PAID ISSUE FEE TOWARD THE ISSUE FEE NOW DUE.

#### HOW TO REPLY TO THIS NOTICE:

I. Review the SMALL ENTITY status shown above.

If the SMALL ENTITY is shown as YES, verify your current SMALL ENTITY status:

A. If the status is the same, pay the TOTAL FEE(S) DUE shown above.

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If the SMALL ENTITY is shown as NO:

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B. If applicant claimed SMALL ENTITY status before, or is now claiming SMALL ENTITY status, check box 5a on Part B - Fee(s) Transmittal and pay the PUBLICATION FEE (if required) and 1/2 the ISSUE FEE shown above.

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III. All communications regarding this application must give the application number. Please direct all communications prior to issuance to Mail Stop ISSUE FEE unless advised to the contrary.

IMPORTANT REMINDER: Utility patents issuing on applications filed on or after Dec. 12, 1980 may require payment of maintenance fees. It is patentee's responsibility to ensure timely payment of maintenance fees when due.

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### Complete and send this form, together with applicable fee(s), to: Mail Mail Stop ISSUE FEE

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

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FISH & RICHA P.O BOX 1022 MINNEAPOLIS	7590 08/18 ARDSON P.C. 5, MN 55440-1022	/2008		Carti	ficato	of Mailing or Trans	mission g deposited with the United st class mail in an envelope above, or being facsimile ate indicated below.
							(Depositor's name)
							(Signature)
							(Date)
APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR		ATTO]	RNEY DOCKET NO.	CONFIRMATION NO.
10/762,153 FITLE OF INVENTION	01/20/2004 : METHOD AND APPA	ARATUS FOR REDUCIN	Xiaopeng Chen IG ECHO AND CROSSTA	ALK IN A COMMU		417/13361-070001 TION SYSTEM	4951
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nonprovisional	NO	\$1440	\$0	\$0		\$1440	11/18/2008
EXAM	INER	ART UNIT	CLASS-SUBCLASS				
PHU, SA	ANH D	2618	370-352000				
"Fee Address" indi PTO/SB/47; Rev 03-0 Number is required.  3. ASSIGNEE NAME A PLEASE NOTE: Unl recordation as set fort!  (A) NAME OF ASSIG	ND RESIDENCE DATA less an assignee is identi h in 37 CFR 3.11. Comp GNEE	"Indication form led. Use of a Customer A TO BE PRINTED ON T ified below, no assignee oletion of this form is NO	(B) RESIDENCE: (CITY	rely, e firm (having as a regent) and the name, reeys or agents. If n printed.  e) atent. If an assignerassignment.  and STATE OR CO	membes of uponam	er a 2	ocument has been filed for
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a. Applicant claims	<b>tus</b> (from status indicated s SMALL ENTITY statu	is. See 37 CFR 1.27.	☐ b. Applicant is no long				
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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/762,153	10/762,153 01/20/2004 Xiaopeng Chen		MP0417/13361-070001	4951	
26200 75	590 08/18/2008		EXAM	INER	
FISH & RICHAI	RDSON P.C.	PHU, SANH D			
P.O BOX 1022 MINNEAPOLIS, MN 55440-1022			ART UNIT	PAPER NUMBER	
			2618		
			DATE MAILED: 08/18/2008		

# **Determination of Patent Term Adjustment under 35 U.S.C. 154 (b)**

(application filed on or after May 29, 2000)

The Patent Term Adjustment to date is 588 day(s). If the issue fee is paid on the date that is three months after the mailing date of this notice and the patent issues on the Tuesday before the date that is 28 weeks (six and a half months) after the mailing date of this notice, the Patent Term Adjustment will be 588 day(s).

If a Continued Prosecution Application (CPA) was filed in the above-identified application, the filing date that determines Patent Term Adjustment is the filing date of the most recent CPA.

Applicant will be able to obtain more detailed information by accessing the Patent Application Information Retrieval (PAIR) WEB site (http://pair.uspto.gov).

Any questions regarding the Patent Term Extension or Adjustment determination should be directed to the Office of Patent Legal Administration at (571)-272-7702. Questions relating to issue and publication fee payments should be directed to the Customer Service Center of the Office of Patent Publication at 1-(888)-786-0101 (571)-272-4200.

	Application No.	Applicant(s)
	10/762,153	CHEN ET AL.
Notice of Allowability	Examiner	Art Unit
	/Sonh D. Dhu/	2619
	/Sanh D. Phu/	2618
The MAILING DATE of this communication apperature All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-85) NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RI of the Office or upon petition by the applicant. See 37 CFR 1.313	(OR REMAINS) CLOSED in this ap or other appropriate communication IGHTS. This application is subject to	plication. If not included n will be mailed in due course. <b>THIS</b>
1. $\boxtimes$ This communication is responsive to <u>the Amendment filed</u>	<u>on 6/24/2008</u> .	
2. $\square$ The allowed claim(s) is/are $\underline{1,2,4-13,16-21,23-32,34-43,45-21}$	- <u>52,54,56 and 58-72</u> .	
3. Acknowledgment is made of a claim for foreign priority ur	nder 35 U.S.C. § 119(a)-(d) or (f).	
a) ☐ All b) ☐ Some* c) ☐ None of the:		
<ol> <li>Certified copies of the priority documents have</li> </ol>	e been received.	
2.  Certified copies of the priority documents have	been received in Application No	
<ol><li>Copies of the certified copies of the priority do</li></ol>	cuments have been received in this	national stage application from the
International Bureau (PCT Rule 17.2(a)).		
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE" noted below. Failure to timely comply will result in ABANDONM THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		complying with the requirements
4. A SUBSTITUTE OATH OR DECLARATION must be subm INFORMAL PATENT APPLICATION (PTO-152) which give		
5. CORRECTED DRAWINGS ( as "replacement sheets") mus	st be submitted.	
(a) $\square$ including changes required by the Notice of Draftspers	on's Patent Drawing Review ( PTO-	-948) attached
1) ☐ hereto or 2) ☐ to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examiner's	s Amendment / Comment or in the C	Office action of
Paper No./Mail Date  Identifying indicia such as the application number (see 37 CFR 1. each sheet. Replacement sheet(s) should be labeled as such in the		
<u> </u>		
<ol> <li>DEPOSIT OF and/or INFORMATION about the depo- attached Examiner's comment regarding REQUIREMENT</li> </ol>		
Attachment(s) 1. ☐ Notice of References Cited (PTO-892)	5. Notice of Informal F	Patent Application
2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. ☐ Interview Summary	• •
•	Paper No./Mail Da	te
3. ☑ Information Disclosure Statements (PTO/SB/08), Paper No./Mail Date <u>5/31/05</u> ; <u>6/6/06</u>	7. 🗌 Examiner's Amendi	ment/Comment
Examiner's Comment Regarding Requirement for Deposit of Biological Material	8. 🛛 Examiner's Stateme	ent of Reasons for Allowance
co.ograd. material	9.	

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1. This Office Action is responsive to the Amendment filed on 6/24/2008.

#### **REASONS FOR ALLOWANCE**

- 2. Claims 1-2, 4-13, 16-21, 23-32, 34-43, 45-52, 54, 56, 58-72 are allowed.
- 3. The following is an examiner's statement of reasons for allowance:

Claims 1-2, 4-13, 16-21, 23-32, 34-43, 45-52, 54, 56, 58-72 are allowable over the prior art of record for the reason as stated in the Applicant's Remark dated on 6/24/2008 pages 16-19 and the reasons as below:

Regarding to claim 1, none of the prior art of record teaches or suggests A transceiver comprising: a digital compensation circuit to generate a digital replica of the interference signal contained in an analog communication signal; a converter to convert the digital replica of the interference signal into a corresponding analog replica of the interference signal; and a subtraction circuit to subtract the analog replica of the interference signal from the analog communication signal, wherein the digital compensation circuit includes a near-end crosstalk (NEXT) canceller to generate a digital replica of a NEXT interference signal in the analog communication signal; wherein the digital compensation circuit further: determines cancellation coefficients that model an impulse response of the interference signal; and multiplies the cancellation coefficients with a communication signal from a transmitter that causes the interference signal. It would not have been obvious for a person skilled in the art to combine other prior arts of record in order to arrive at the claimed invention.

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Regarding to claim 12, none of the prior art of record teaches or suggests a method for reducing interference signals in an analog communication signal, the method comprising: generating a digital replica of the interference signal contained in an analog communication signal; converting the digital replica of the interference signal into a corresponding analog replica of the interference signal; and subtracting the analog replica of the interference signal from the analog communication signal to substantially cancel the interference signal from the analog communication signal, wherein the interference signal includes a NEXT interference signal, and wherein generating a digital replica of the interference signal includes: determining cancellation coefficients that model an impulse response of the interference signal; and multiplying the cancellation coefficients with a communication signal from a transmitter that causes the interference signal. It would not have been obvious for a person skilled in the art to combine other prior arts of record in order to arrive at the claimed invention.

Regarding to claim 20, none of the prior art of record teaches or suggests a transceiver comprising: generating means for generating a digital replica of the interference signal contained in an analog communication signal; converting means for converting the digital replica of the interference signal into a corresponding analog replica of the interference signal; and subtracting means for subtracting the analog replica of the interference signal from the analog communication signal to substantially cancel the interference signal from the analog communication signal, wherein the generating means includes means for generating a digital replica of a NEXT interference signal in the analog communication signal, and wherein the generating

means further: determines cancellation coefficients that model an impulse response of the interference signal; and multiplies the cancellation coefficients with a communication signal from a transmitter that causes the interference signal. It would not have been obvious for a person skilled in the art to combine other prior arts of record in order to arrive at the claimed invention.

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Regarding to claim 31, none of the prior art of record teaches or suggests a network device in a communication system, the network device comprising: transceiver, the transceiver including, a digital compensation circuit to generate a digital replica of the interference signal contained in an analog communication signal; a converter to convert the digital replica of the interference signal into a corresponding analog replica of the interference signal; and a subtraction circuit to subtract the analog replica of the interference signal from the analog communication signal, wherein the digital compensation circuit includes a NEXT canceller to generate a digital replica of a NEXT interference signal in the analog communication signal, and wherein the digital compensation circuit further: determines cancellation coefficients that model an impulse response of the interference signal; and multiplies the cancellation coefficients with a communication signal from a transmitter that causes the interference signal. It would not have been obvious for a person skilled in the art to combine other prior arts of record in order to arrive at the claimed invention.

Regarding to claim 42, none of the prior art of record teaches or suggests a network device in a communication system, the network device comprising:

communication means, the communication means including, generating means for generating a digital replica of the interference signal contained in an analog communication signal; converting means for converting the digital replica of the interference signal into a corresponding analog replica of the interference signal; and subtracting means for subtracting the analog replica of the interference signal from the analog communication signal to substantially cancel the interference signal from the analog communication signal, wherein the generating means includes means for generating a digital replica of a NEXT interference signal in the analog communication signal, and wherein the generating means further: determines cancellation coefficients that model an impulse response of the interference signal; and multiplies the cancellation coefficients with a communication signal from a transmitter that causes the interference signal. It would not have been obvious for a person skilled in the art to combine other prior arts of record in order to arrive at the claimed invention.

Regarding to claim 54, none of the prior art of record teaches or suggests a cancellation system for use in a communication system including a communication line, the communication line having a transmitter and a receiver at each end, the cancellation system to reduce interference signals in an analog communication signal received by a receiver, the cancellation system comprising: a NEXT canceller, the NEXT canceller operable to generate a digital replica NEXT interference signal; a converter to convert the digital replica of the NEXT interference signal into a corresponding analog replica of the NEXT interference signal; and a subtracter to subtract the replica NEXT interference signal from an analog communication signal received by the receiver; wherein the

NEXT canceller is further operable to: determine cancellation coefficients that model an impulse response of an interference signal; and multiply the cancellation coefficients with a communication signal from the transmitter. It would not have been obvious for a person skilled in the art to combine other prior arts of record in order to arrive at the claimed invention.

Regarding to claim 56, none of the prior art of record teaches or suggests a cancellation system for use in a communication system including a communication line, the communication line having a transmitter and a receiver at each end, the cancellation system to reduce interference signals in an analog communication signal received by a receiver, the cancellation system comprising: NEXT cancellation means, the NEXT cancellation means for generating a digital replica NEXT interference signal based on the transmitted signal; converting means for converting the digital replica of the NEXT interference signal into a corresponding analog replica of the NEXT interference signal; and subtracting means for subtracting the replica NEXT interference signal from an analog communication signal received by the receiver~ and wherein the NEXT cancellation means further: determines cancellation coefficients that model an impulse response of an interference signal; and multiplies the cancellation coefficients with a communication signal from the transmitter. It would not have been obvious for a person skilled in the art to combine other prior arts of record in order to arrive at the claimed invention.

Regarding to claim 58, none of the prior art of record teaches or suggests a method for reducing interference signals in an analog communication signal received by

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a receiver of a communication line, the method comprising: generating a digital replica NEXT interference signal based on the transmitted signal; converting the digital replica of the NEXT interference signal into a corresponding analog replica of the NEXT interference signal; and subtracting the replica NEXT interference signal from an analog communication signal received by the receiver; and wherein generating a digital replica NEXT interference signal includes: determining cancellation coefficients that model an impulse response of an interference signal; and multiplying the cancellation coefficients with a communication signal from the transmitter. It would not have been obvious for a person skilled in the art to combine other prior arts of record in order to arrive at the claimed invention.

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Regarding to claim 59, none of the prior art of record teaches or suggests a transceiver comprising: a receiver to receive an analog communication signal, the analog communication signal containing a plurality of interference signals; a digital compensation circuit to generate a digital replica of each interference signal contained in the analog communication signal; a combiner to combine each digital replica to generate a combined digital replica; a converter to convert the combined digital replica into a corresponding analog replica of the interference signal; and a subtraction circuit to subtract the analog replica from the analog communication signal; wherein the digital compensation circuit further: determines cancellation coefficients that model an impulse response of an interference signal; and multiplies the cancellation coefficients with a communication signal from a transmitter. It would not have been obvious for a person

skilled in the art to combine other prior arts of record in order to arrive at the claimed invention.

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Regarding to claim 60, none of the prior art of record teaches or suggests a method for reducing interference signals in an analog communication signal, the method comprising: receiving an analog communication signal through a receiver, the analog communication signal containing a plurality of interference signals; generating a digital replica of each interference signal contained in the analog communication signal; combining the digital replica of each interference signal to generate a combined digital replica; converting the combined digital replica into a corresponding analog replica of the interference signal; and subtracting the analog replica from the analog communication signal to substantially cancel each interference signal from the analog communication signal; wherein generating a digital replica of each interference signal includes: determining cancellation coefficients that model an impulse response of an interference signal; and multiplying the cancellation coefficients with a communication signal from a transmitter. It would not have been obvious for a person skilled in the art to combine other prior arts of record in order to arrive at the claimed invention.

Regarding to claim 61, none of the prior art of record teaches or suggests a network device in a communication system, the network device comprising: a transceiver operable to receive an analog communication signal containing a plurality of interference signals, the transceiver including, a receiver to receive the analog communication signal; a digital compensation circuit to generate a digital replica of each interference signal contained in the analog communication signal; a combiner to

combine the digital replica of each interference signal to generate a combined digital replica; a converter to convert the combined digital replica into a corresponding analog replica of the interference signal; and a subtraction circuit to subtract the analog replica of the interference signal from the analog communication signal; wherein the digital compensation circuit further: determines cancellation coefficients that model an impulse response of an interference signal; and multiplying the cancellation coefficients with a communication signal from a transmitter. It would not have been obvious for a person skilled in the art to combine other prior arts of record in order to arrive at the claimed invention.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### Conclusion

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Sanh D. Phu whose telephone number is (571)272-7857. The examiner can normally be reached on M-Fr from 8:00-16:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nay Maung can be reached on (571) 272-7882. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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/Sanh D Phu/ Primary Examiner Art Unit 2618